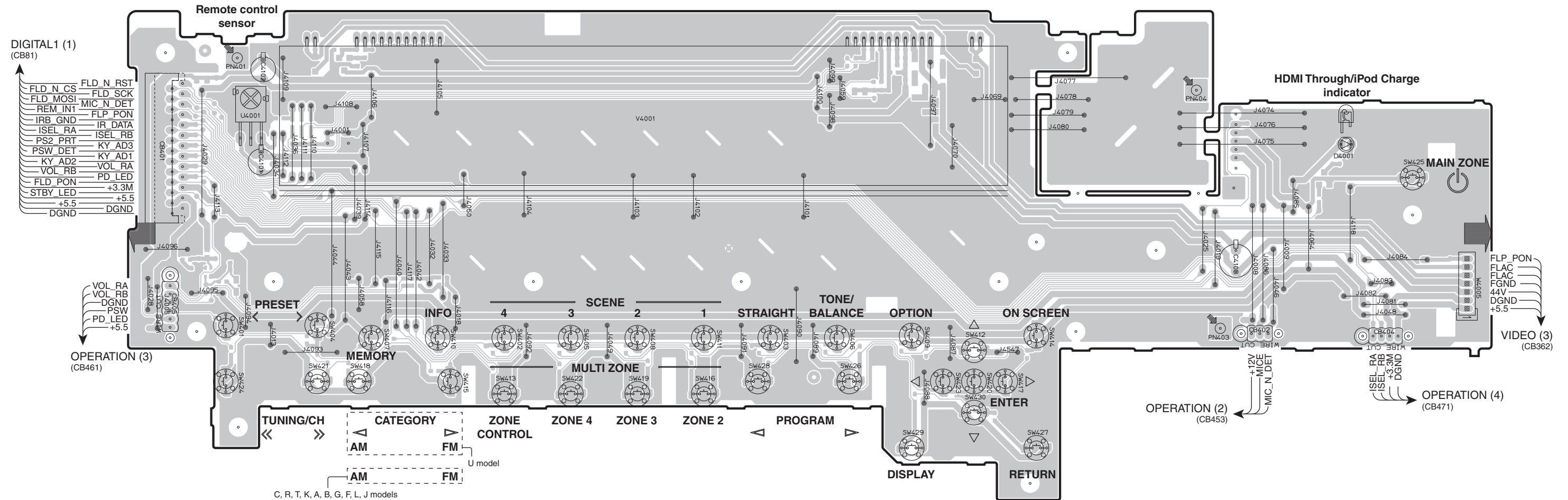


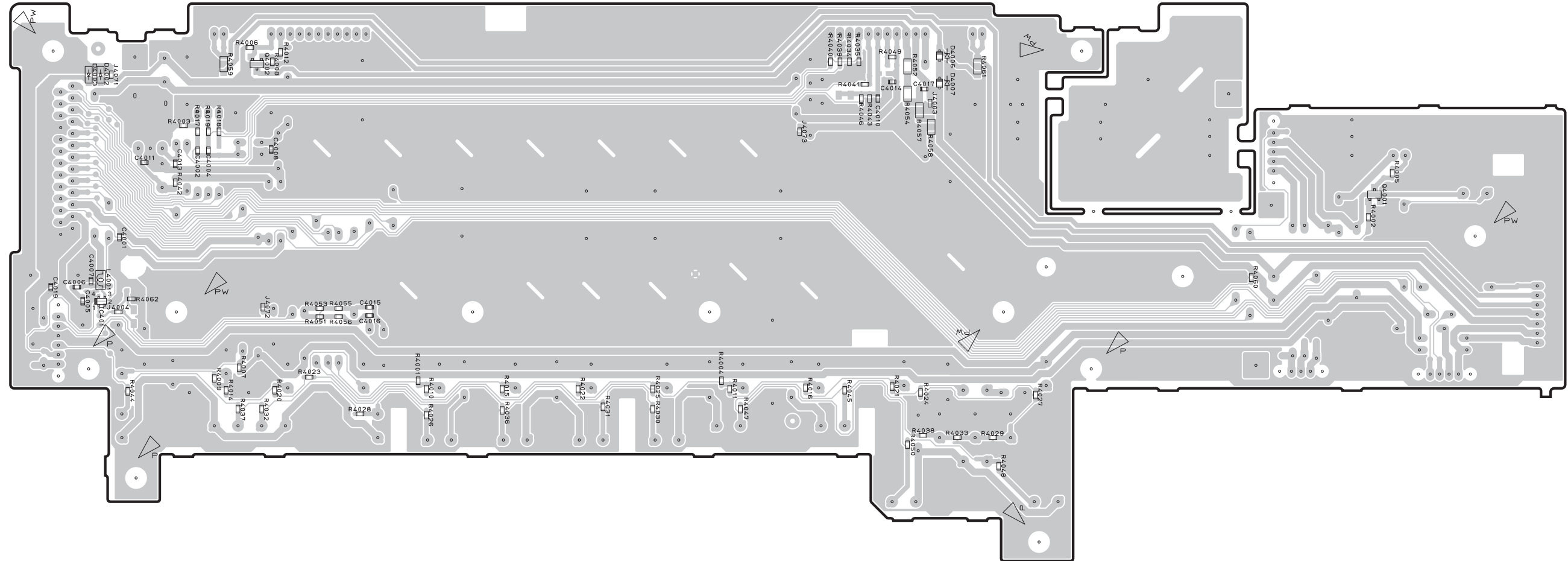
OPERATION (1) (Side A)



- Semiconductor Location

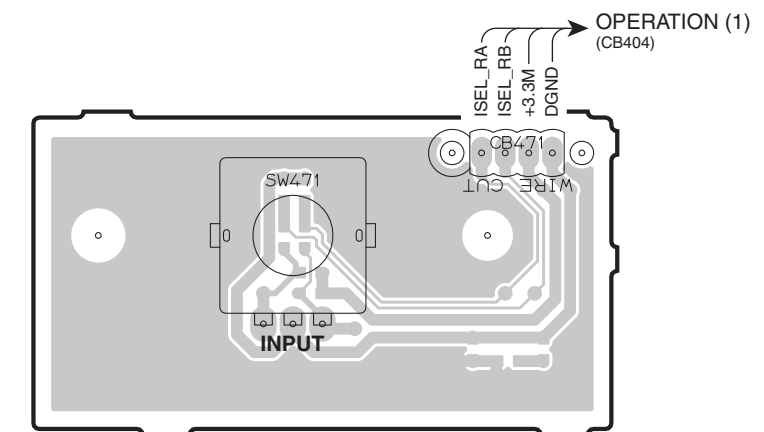
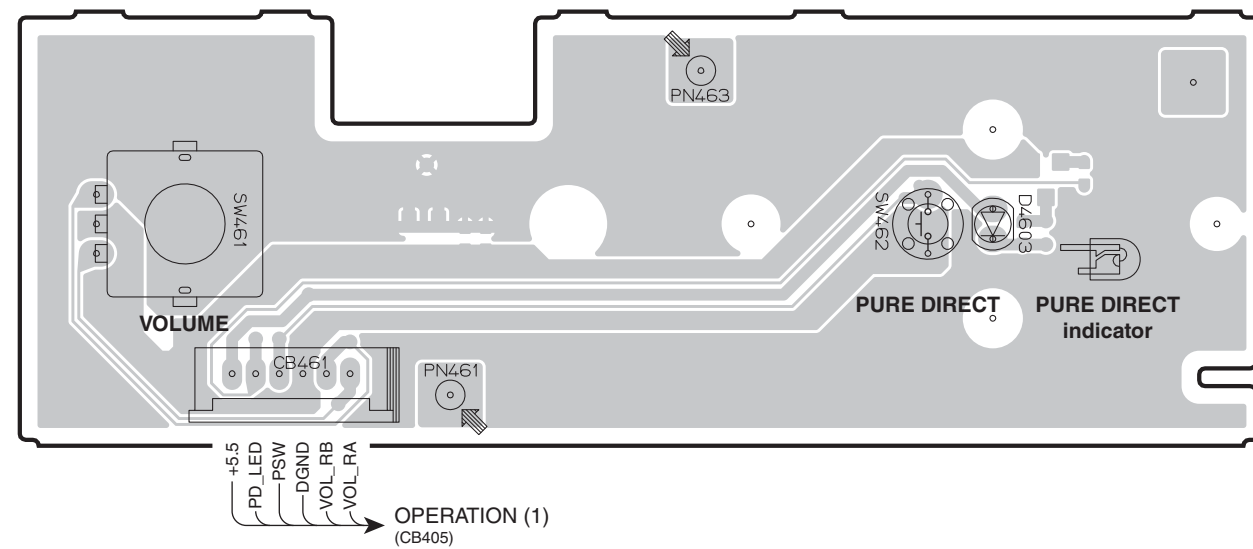
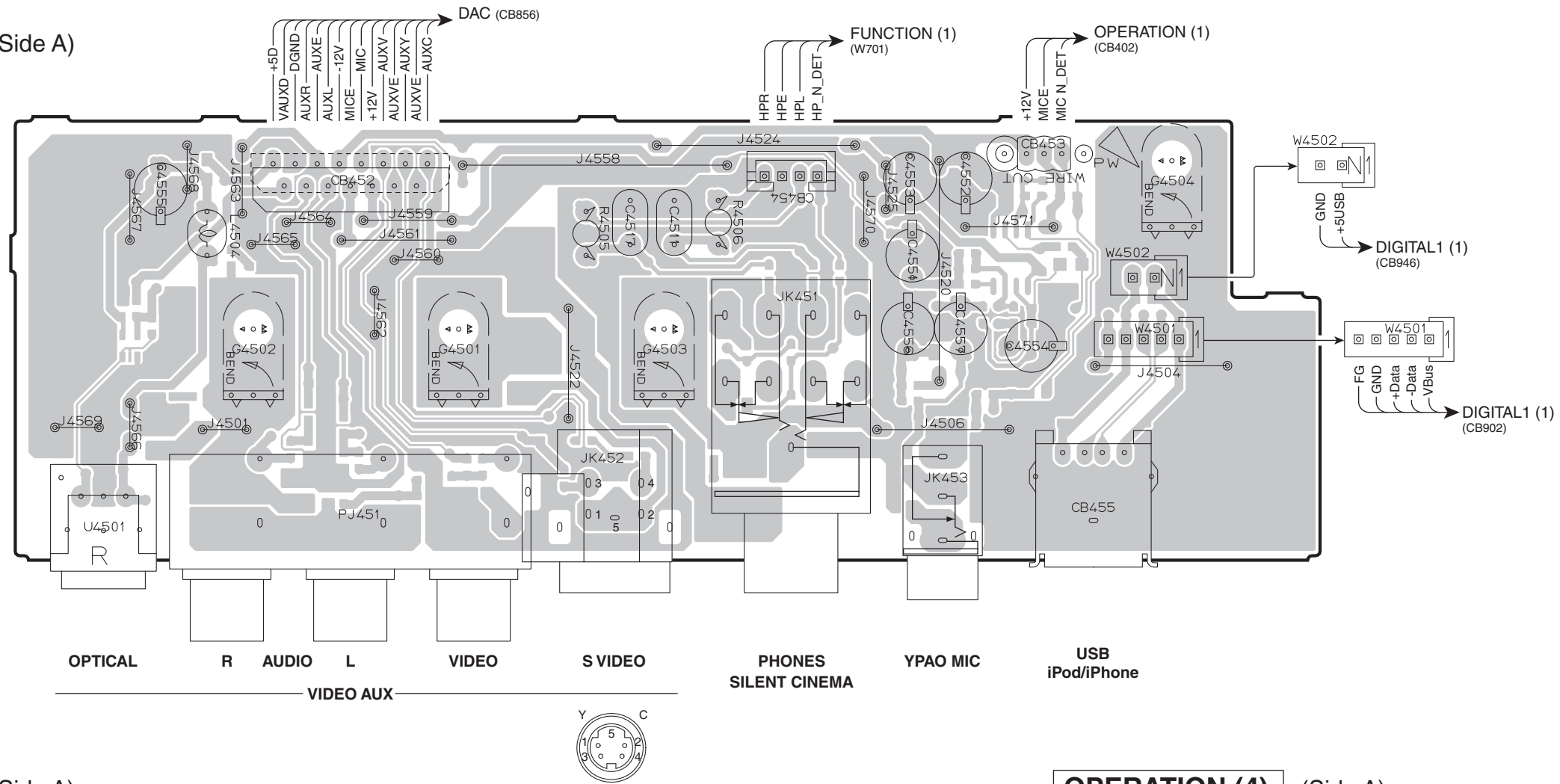
Ref no.	Location
D4001	I3

OPERATION (1) (Side B)



- Semiconductor Location

Ref no.	Location
D4002	B3
D4003	A3
D4006	F3
D4007	F3
IC401	B4
Q4001	H3
Q4002	B3



- Semiconductor Location

Ref no.	Location
D4603	E6

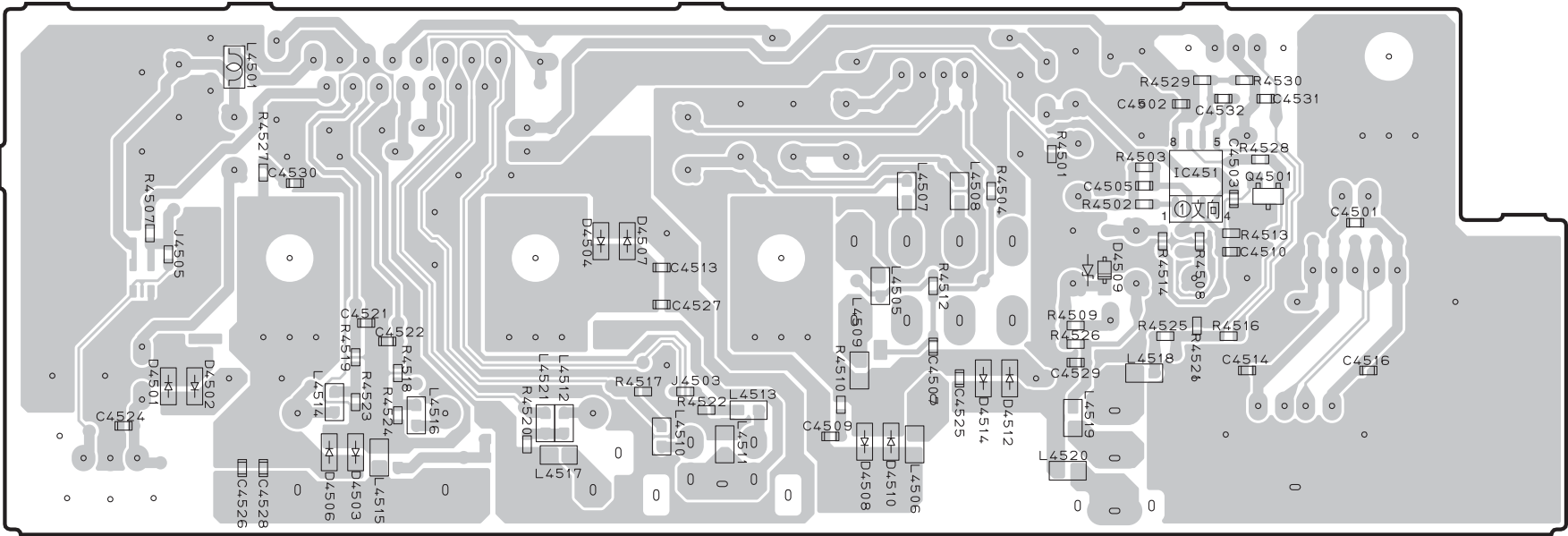
1

OPERATION (2) (Side B)

2

3

4



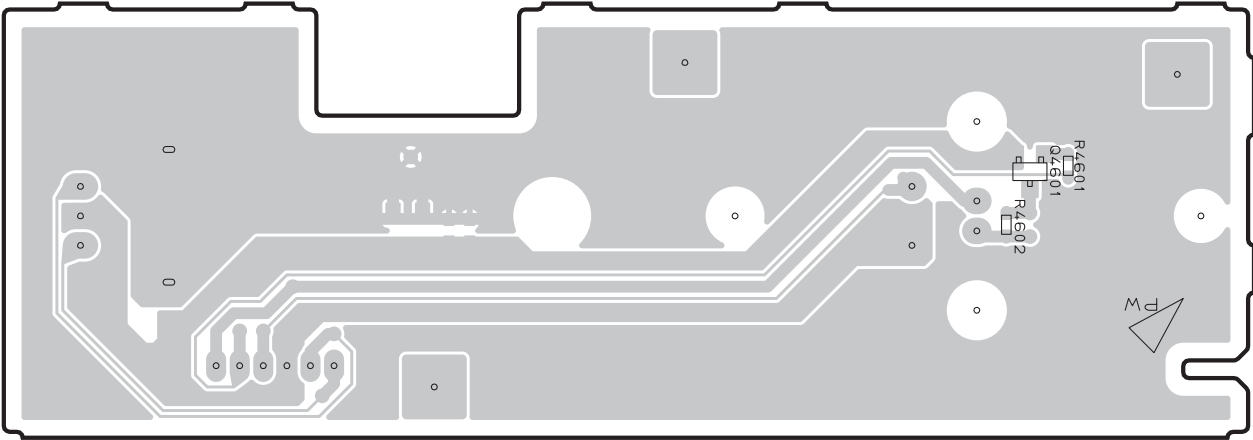
• Semiconductor Location

Ref no.	Location
D4501	C3
D4502	C3
D4503	D3
D4504	D3
D4506	C3
D4507	E3
D4508	E3
D4509	F3
D4510	E3
D4512	F3
D4514	F3
IC451	G2
Q4501	G2
Q4601	E6

5

OPERATION (3) (Side B)

6



7

OPERATION (4) (Side B)

